

PATENT ABSTRACTS OF JAPAN

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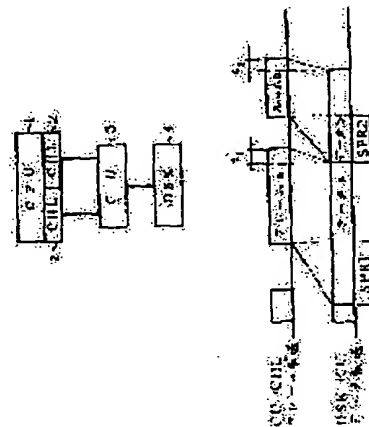
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(54) CONTROL SYSTEM FOR TRANSFER OF DATA

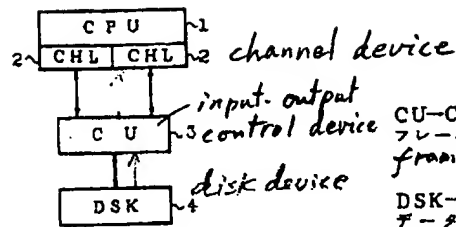
(57)Abstract:

PURPOSE: To minimize the variance of time between the transfer end of a disk device and that of a controller by starting the frame transfer when the coincidence is obtained between the data quantity of a buffer memory and the contents of a pointer showing the data transfer starting position.

CONSTITUTION: The frame transfer between a controller 3 and a channel device 2 and the data transfer between a disk device 4 and the controller 3 are carried out by the shown timing chart. The frame transfer is started at a time point when the data having the number of bytes shown by a start pointer for the basic frame is stored with a basic frame (a), and the data of the number of bytes shown by a start pointer for the final frame is stored with the final frame (b) respectively. When the frame transfer start point is set to both start pointers in response to the difference of transfer speed between the device 4 and the device 2. Thus the same hardware is available even with plural disk devices having different transfer speeds.



第 2 図



第 3 図

